Atal Bihari Vajpayee-Indian Institute of Information Technology and Management
(An Institute of National Importance, Ministry of Education, Govt. of India)

INVITING APPLICATION FOR VRITIKA RESEARCH INTERNSHIP UNDER SERB ACCELERATE VIGYAN SCHEME
on

DESIGN AND ANALYSIS OF MULTI-HOP AND MESH HYBRID NETWORKS FOR HIGH-RATE RELIABLE B5G COMMUNICATION

(5th Aug. to 4th Sept., 2024)

About the Institute

Atal Bihari Vajpayee-Indian Institute of Information Technology and Management Gwalior (ABV-IIITM Gwalior) is an apex Information Technology (IT) and Management Institute, established by the Government of India. ABV-IIITM Gwalior has been declared an Institute of National Importance. The Institute strives to become a world-class Institution that endeavors to carve young minds through teaching and research and develop them as tomorrow's leaders. At present Institute has five departments.

The Department of Electrical and Electronics Engineering is one of the major departments being created in 2022 to achieve the mission and vision of the institute. The Department is currently in its development phase and engaged in teaching, research, and development in the areas of VLSI, real-time embedded systems, wireless communication, multimedia signal processing, power systems and control, and high-frequency communication systems. Currently, the department offers a B.Tech in Electrical and Electronics Engineering, M. Tech. in VLSI and Embedded Systems, and a Ph.D. program in different areas such as VLSI, audio signal processing, digital image processing, wireless communication, antenna design, power system & control, and other allied areas. The faculty members of the department are actively engaged in outreach activities by organizing workshops, faculty development programs, conferences, and academic collaboration. For more details, visit the link: **Department of EEE**

About the Vritika

VRITIKA' is the call for initiation and practice in science through Training and Skill Internship. This program aims to provide opportunities to promising PG students from universities and colleges to get exposure and hands-on research skill development experience. These internships will primarily be facilitated by organizations/ institutions/ laboratories of national importance. For further details, visit the links: **SERB Vritika**

Program Objective

This internship program aims to train and groom young researcher pursuing their research career in wireless communication at UG, PG, and PhD levels. The internship aims to develop an understanding of current and future trends in B5G wireless communications. The objective is to introduce the latest wireless technologies, i.e., FSO, (sub)-THz, mmWave communications, and their applicability to design various dual/multihop hybrid wireless communication systems for different applications.

Eligibility Criteria

- Regular UG (final year)/MTech/M.E./M.S/PhD) students pursuing their degree in the area of ECE/CSE/IT or any relevant branch within India are eligible to apply.
- The applicants must produce a letter of authentication from their Supervisor/Head of the Department/Head of the Institute indicating their association with the Institute and a "No Objection Certificate (NOC)" on the institute/university letterhead for allowing their student to undergo training in the workshop if selected.

Nature of Support and other Terms and Conditions

- The supervisor to whom the interns are assigned would be assigning them tasks/assignments as per the research modules designed.
- The intern should work accordingly and prepare a work report to be submitted after completion of the internship.
- The IPR of the research carried out during the internship will be reserved by the institute, and cannot be used by the intern without permission of the concerned authority.
- Hostel accommodation with catering facilities and travel by train may be provided to the candidates as per the SERB norms.
- Selected candidates are required to attend the program physically. 100% attendance is mandatory.
- The intern should abide by the rules and regulations of the institution and laboratory protocols.

Registration Detail

- The interested candidate shall apply for the internship program by filling out the following Google Form: **https://forms.gle/64Qg3qziGkMnidCS6.** Registration form with the requested details, scanned copy of the certificates, letter of authentication, and "No Objection Certificate (NOC) can be uploaded through the link mentioned above.
- A maximum of 05 interns will be selected based on the recommendation of the selection committee of the Electrical and Electronics Engineering Department.
- The selected interns must acknowledge and accept the offer by email within 48 hours, failing which the waitlisted candidate will get the offer.

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- **NO REGISTRATION FEE**
- **LAST DATE OF REGISTRATION: 30 JULY 2024**