

Advt. No.:3684

ABV-Indian Institute of Information Technology and Management Morena Link Road, Gwalior - 474 015, Madhya Pradesh (An Institute of National Importance, MoE, GoI) <u>https://www.iiitm.ac.in</u> Telephone: 9586288218, 9109106995 Email: eephd@iiitm.ac.in

Date: 24/05/2024

Admission to Ph.D. (Full-Time) under Visvesvaraya Ph.D. Scheme Phase –II

Applications are invited for admission to Ph.D. (Full-Time) Programme under Visvesvaraya Ph.D. Scheme Phase-II. The Broad areas of Research, Eligibility Criteria, Selection Procedure, Teaching Assistantship-cum-Scholarships etc. are given in this advertisement.

How to apply: The link to fill the online application form can be accessed through Institute website <u>www.iiitm.ac.in</u> or directly apply at <u>https://forms.gle/i56QVQsqkbQyUk8F6</u>. An application fee of Rs.500/- (Rs.250/- for SC/ST/PWD candidates) is required to be paid online. The candidates are required to apply online and there is no requirement to send the Hard Copy of the application form to the Institute. Application fee shall not be refunded in any case.

Start date for Applications 25th May 2024, and last date for submission of application form is 20th June 2024.

For more details, the candidates are advised to visit the Institute website www.iiitm.ac.in for updates. All updates regarding this Advertisement will be updated on the Institute website only. All candidates are advised to visit the Institute website regularly.



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A) Broad Areas of Research

Sr. No.	Department	Broad Areas of Research	Number of Seats
1.	Electrical and Electronics Engineering	VLSI and Embedded Systems, Electronics, Intelligent Sensing, Audio, Image, Biomedical & Signal Processing, Wireless Communication, Artificial Intelligence & Machine Learning, Molecular & Optical Communication, Next-Generation Antenna Technology, Microwave Communication etc.	03
2.	Under Project	Only Under Chips to Startup (C2S) Programme under Project "Implantable Pacemaker Chip (iPACE- CHIP)" and "Designing energy-efficient AI accelerator for 3D object detection"	02

B) Eligibility Criteria for admission to Full-Time Ph.D. programs:

Qualifying Degree of the Prospective CandidatesMinimum CGPA requirement for General/ OBC/ EWS CandidatesMinimum CGPA requirement for SC/ ST/ PWD CandidatesM. Tech./ M. E./ M. S./M. S. (by research) or equivalent post graduate degrees in Engineering/Technology or any other equivalent qualification . Additionally, the applicants must have qualified the GATE/NET exam.A minimum of 65% marks or cGPA of 7.00 on a 10-point scale in the qualifying degreeA minimum of 60% marks cGPA of 6.5 on a 10-point scale in the qualifying degreeM. Sc./MCA or equivalent post- graduate degree. Additionally, the applicants post- graduate degree. Additionally,A minimum of 65% marks or CGPA of 7.00 on a 10-pointA minimum of 60% marks cGPA of 6.5 on a 10-point	b) Englomity Criteria for admission to Fun-Time Th.D. programs.						
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other equivalent qualification. scale in the qualifying degree scale in the qualifying degree	e						
Additionally, the applicants must							
have qualified the GATE/NET							
exam \$\$							

\$\$ The requirement for GATE can be exempted for the applicants having B.Tech. or Equivalent degree from Centrally Funded Technical Institutions (CFTIs) with CGPA 8.0 or above at B.Tech. level.



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C) Fellowship Amount and other Benefits

Fellowship	Rs. 38750/- (1st and 2nd year) Rs. 43750/- (3rd, 4th and 5th year)
Reimbursement of House Rent	As per Funding Agency Norms
Research Contingency grant	Rs. 1,20,000/ year

Other/detailed Terms and Conditions: As per Visvesvaraya Ph.D. Scheme Phase –II, Round - II funded by Ministry of Electronics and IT (MeitY).

- Support for attending International Conference: up to 1.5 lakhs/Full-time PhD candidate.
- **Visit to Labs abroad**: The support would be available from 3rd year of PhD to the selected Full Time PhD candidates. For more details: <u>https://PhD.digitalindiacorporation.in/home</u>
- D) Nodal Officer: Prof. Manisha Pattanaik (EEE Department), Email: <u>eephd@iiitm.ac.in</u> Coordinator: Dr. Vinal Patel. (EEE Department), Contact: Ph. No.: 9586288218, <u>vp@iiitm.ac.in</u>

E) Admission Procedure

- i. The applicants are required to fill out the online application form available at <u>https://forms.gle/i56QVQsqkbQyUk8F6</u>. It is not required to send the form by post. However, the applicant must bring a self-attested copy of the original application form at the time of the interview.
- ii. Selection of shortlisted applicants will be based on previous academic records and/or performance in the presentation-cum-interview. The shortlisting criteria may be higher than the minimum eligibility requirements. The admission shall be solely based upon the performance of individuals during "presentation and interaction".
- iii. The Department Admission Committee for the Department shall recommend the suitable candidates for admission based on its assessment of presentation and interaction.
- iv. The Department Admission Committee /Institute reserve the right not to recommend any candidate for admission to Ph.D. in the respective Department if the performance of the shortlisted candidates is not found satisfactory during "presentation and interaction". The decision of Department Admission Committee (approved by the competent authority of the Institute) shall be final.



- v. The merit list of the selected candidates (based on presentation & interaction) shall be prepared on marks basis. A candidate who scores less than 40 marks (out of 100) for "presentation & interaction" shall not be considered "qualified" for admission to Ph.D. programme and his/her name shall not be recommended for admission by Department Admission Committee.
- vi. The list of recommended candidates for admission to Ph.D. (Full-Time) programme shall be made in order of merit.
- vii. All the admitted candidates shall be governed by the Ph.D. regulations of the Institute, guidelines of the Visvesvaraya Ph.D. Scheme Phase –II funded by Ministry of Electronics and IT (MeitY) and other instructions issued by the Institute time to time as well as the Ministry of Electronics and IT (MeitY).

F) Other Issues

i. Category (UR/EWS/OBC/SC/ST/PWD) once chosen by the applicant in his/her application form shall not be changed at a later stage. The candidate (s) applying under (EWS/OBC/SC/ST/PWD) category shall have to produce a valid category certificate issued by the competent authority.

ii. OBC-NCL/EWS Certificate must have been issued on or after 1st April, 2024 so that the candidates from creamy layer are identified. No certificate issued before this date shall be acceptable.

iii. All the candidates should ensure that they possess the required educational qualification by the last date of submission of the application under this Advertisement.

iv. Certificate Checking: The certificates (in original) of all the candidates recommended for admission shall be checked by the individual Department before deposition of fee by the candidates.

vi. The application without proof of application fee as applicable/self-attested copies of documents/certificates/testimonials shall be rejected and shall not be considered for shortlisting.

Fee Details: (https://www.iiitm.ac.in/images/2023/June_2023/Fee_Structure_July_2023.pdf)



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G) Fellowship

The candidates admitted under this Advertisement will be eligible for fellowship as mentioned in the Table at C) above. Fellowship shall be paid as per the guidelines of the Visvesvaraya Ph.D. Scheme Phase -II funded by Ministry of Electronics and IT (MeitY) and other instructions issued by the Ministry of Electronics and IT (MeitY) in this regard. The period of the project is 05 years or till the completion of the project whichever is earlier. Fellowship will be started from the date of joining by the candidate in the Department and fellowship will be paid for a maximum duration of 05 Years or till the completion of project whichever is earlier (as per project guidelines).

H) Important Dates

Sr. No.	Activity	Date
1.	Start date for Applications*	25 th May 2024
2	Last date for submission of Applications	20 th June 2024
3	Display of list of candidates eligible for presentation & interaction	22 th June 2024
4	Presentation/interaction by all eligible candidates	29 th June 2024
5	Display of list of selected candidates	1 st July 2024
6	Last Date of Deposition of fee	5 th July 2024
7	Last Date of Registration	23 rd July 2024
8	Commencement of classes	Will be notified separately by the Department

*if required 2nd round for PhD admission will be conducted under Visvesvaraya Ph.D. Scheme Phase –II.

I) All disputes pertaining to the admissions shall fall within the jurisdiction of Gwalior only.

J) All admissions will be provisional till these are confirmed subject to medical fitness, payment of all the fees, fulfillment of eligibility conditions, and verifications of certificates by the Academic Section of the Institute.

K) After payment of fee (application/admission fee), no fee refund request shall be entertained in any case.



Use the following account for the application fee payment: Account Name: Director ABV-IIITM Gwalior Account Number: 945210110000969 IFSC Code: BKID0009462 Bank: Bank of India Branch: ABV-IIITM Gwalior Branch

L) The Institute reserves the right to modify or cancel this Advertisement/any part of this Advertisement at any stage.

In case you have any queries related to the Visvesvaraya Ph.D. Programme admission, you may contact at EEE Department, Email: <u>eephd@iiitm.ac.in</u>, <u>manishapattanaik@iiitm.ac.in</u> or 0751-2449809 (10:00 AM – 06:00 PM), 9109106995, 9586288218.

Registrar