

ABV-INDIAN INSTITUTE OF INFORMATION TECHNOLOGY AND MANAGEMENT, GWALIOR

Department of Electrical and Electronics Engineering

7-Days Self-Sponsored Hybrid mode Workshop

On

Analog IC Design: Schematic to Layo

December 17-23, 2024

About the Institute

ABV-IIITM Gwalior is a premier institute incepted by MHRD, Government of India, in the year 1997 as a center of excellence in the field of Information Technology and Management. It is NAAC 'A' certified and has also been declared as an institute of national importance by the Government of India. It is located in the city of Gwalior in the northern part of the state of Madhya Pradesh, India. For more information, please visit: http://iiitm.ac.in/

Topics to be covered

Mode of the course: Online/Offline

	Session 1 11:00 AM - 12:30 PM	Session 2 2:00 PM- 5:30 PM (Hands-on)
Day 1	Inauguration and Expert talk by Prof. Hitesh Shrimali (IIT Mandi) 09:30 AM - 12:30 AM	Common source amplifier design and analysis
Day 2	Prof. Hitesh Shrimali (IIT Mandi), OP-AMP Design	Differential Amplifier design and Layout Design
Day 3	Dr. Gaurav Kaushal (IIITM Gwalior), OP-AMP Design	Current mirror design and Layout Design
Day 4	Dr. Gaurav Kaushal (IIITM Gwalior) ADC Design	Op-amp Design-1
Day 5	Dr. Abhishek Kumar (Wipro) ADC Design	Layout Design of Op- amp Design
Day 6	Mr. Sanjay Kumar (NSING Singlpore), BGR	Mr. Sanjay Kumar (NSING Singlpore)LDO, and Layout Design
Day 7	Talk by Prof. Rajesh Khatri (SGSITS Indore), ADC and Quiz	Case Study: LDO and Valedictory

Study materials and Certificate will be provided to all the participants. The student will be technically benefited for internship and job placement as Analog Engineer in Analog VLSI Companies /Projects/Higher studies.

Resource Persons

Faculty members from IITs, IIITs and Industry experts shall deliver lectures and hands-on.

Objectives

Enhance the knowledge and skills of the participants in various steps of Analog IC Design. It will cover basic fundamentals on differential amplifier, op-amp design, low drop out regulator ADC Design followed by pre layout and post layout simulation.

Provides a hands-on training to get real-time knowledge about Analog Layout with the help of industry experts.

Registration Fee

Student (online/Offline) : INR (2200/2500)/-Faculty Members (Online/Offline) : INR (2500/2700)/-

Faculty Early Bird registration before or on 10 December 2024 (Online/Offline) : INR (2200/2500)/-

Industry : INR 3000/-

The lodging and food facility may be provided in the hostel/guest house as per the institute norms by their own expense. Account Name : Director, ABV-IIITM, Gwalior Account Number : 945210110009380 Bank name : Bank of India & IFSC Code : BKID0009462

Branch Address : IIITM Branch Gwalior

Chief Patron: Prof. Sri Niwas Singh Director, ABV-IIITM Gwalior Patron: Prof. Manisha Pattanaik H.O.D., Electrical and Electronics Engineering Course Coordinators:

Dr. Gaurav Kaushal, Dr. Somesh Kumar , Dr. Alok Kumar kamal, Dr. Biswabandu Jana,

Registration Link

https://forms.gle/sNakB4CxvooyDF7K6

Scan here for registration

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