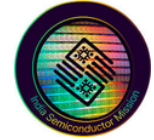


7 Days workshop on Exploring FPGAs for Rapid Prototyping of Digital Systems
2 - 8 May 2025 (Hybrid mode)

with support from



विश्वजीवनामृतं ज्ञानम्

About The Department

Electrical and Electronics Engineering department was established in the year 2022, Previously it was under Information Technology department established in the year 1997. Since the inception, it is consistently creating its place of excellence not only within the institute but among its counter parts in the country and abroad. Bright students from all the parts of the country for quality education in IT & EC at under graduate, post graduate, and doctoral levels are attracted. The department has highly qualified and competent faculty members, and adequate facilities to support teaching and learning activity.

Course Objectives

This course aims to provide participants with practical, hands-on experience in FPGA-based digital system design. It covers the fundamentals of hardware description languages (HDL), FPGA architecture, and advanced digital design techniques, equipping learners to develop, implement, and optimize digital circuits on FPGA platforms.

Registration Process

- **Register using the link below:**
<https://forms.gle/byDMFr6FpaXBbGXY6>
- **Eligibility:** UG, PG, Faculty, PhD. Scholars
- **Registration Fees:** Rs. 1000/- (for Students) and Rs. 1500/- (for Faculty) (Hybrid mode)



Last date of Registration: April 10, 2025

Bank Details for Payment

- **Account Name:** ABV IIITM FDP Account
- **Account Number:** 945210110009380
- **Name of the Bank:** Bank of India
- **IFSC Code:** BKID0009462
- **Bank address and Branch:** IIITM Branch, Gwalior

Come, Learn, Win the
Participants: contest

Contact Person

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Mob: +91-9062325746

Topics to be Covered

• Theory Sessions (not limited to)

Digital System Design, FPGA Architecture and Programming Models, FPGA Development Tools, Hardware Implementation, Advanced FPGA Applications

• Hands-On Sessions: .

The hands-on sessions will provide participants with practical experience in FPGA-based system design, allowing them to implement, test, and optimize real-world applications.

Day	Theory	Hands on
Day-1	Introduction to Verilog HDL	Hands on Basis3 / Pynq board
Day-2	Digital Circuits And Logic Design	
Day-3	VLSI Design - Digital System I	
Day-4	VLSI Design - Digital System II	
Day-5	Advanced Digital Integrated Circuit Design I	
Day-6	Advanced Digital Integrated Circuit Design II (Implementation of algorithm)	Design Contest, Quiz & Certification
Day-7	RTL to GDS flow	

Resource Persons

Faculty Members from IITs, IISc, and Industry experts shall deliver lectures and hands-on.

Course Coordinators

- Dr. Biswabandhu Jana, EEE, ABV- IIITM Gwalior
- Dr. Gaurav Kaushal, EEE, ABV- IIITM Gwalior
- Prof. Manisha Pattanaik , EEE, ABV- IIITM Gwalior